Attorney's Docket No.: 01568-006004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mark W. Miles
Serial No. :
Filed : February 19, 2002
Title : INTERFEROMETRIC MODULATION OF RADIATION

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Preliminarily, please amend the application as follows:

In the specification:

Replace the paragraph beginning at page 1, line 5 with the following rewritten paragraph:

-- This application is a divisional and claims priority from U.S. application 08/769,947, filed December 12, 1996, which is a continuation-in-part of both U.S. application No. 08/238,750, filed May 5, 1994, now issued as U.S. Patent No. 5,835,255, and U.S. application No. 08/554,630, filed November 6, 1995, now abandoned (both incorporated herein by reference). --

Insert the following paragraph beginning at page 1, line 23:

--One such design includes a filter described as a hybrid filter which has a narrow bandpass filter and an induced absorber. When the wall associated with the hybrid filter is brought into contact with a reflector, incident light of a certain range is absorbed. This occurs

CERTIFICATE OF MAILING BY EXPRESS MAIL

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February 19, 2002
Date of Deposit

Signature

Leroy Jenkins
Typed or Printed Name of Person Signing Certificate

because the induced absorber matches the impedance of the reflector to that of the incident medium for the range of frequencies passed by the narrow-band filter. --

Insert the following paragraphs beginning at page 12, line 32:

--Fig. 36 is a diagram of layers of a modulator.

Fig. 37 is a perspective view of cavities in a device.

Fig. 38 is a diagram of a side view of a pixel device.

Fig. 39 is a graph of the optical response for a cavity which appears black.

Fig. 40 is a graph of the optical response for a cavity which appears blue.

Fig. 41 is a graph of the optical response for a cavity which appears green.

Fig. 42 is a graph of the optical response for a cavity which appears red.

Fig. 43 is a graph of the optical response for a cavity which appears white.

Fig. 44 is a perspective view of a fragment of a reflective flat panel display.

Figs. 45A, 45B, 45C, and 45D are perspective views of different spacers during fabrication.

Figs. 46A, 46B, 46C, and 46D are also perspective views of different spacers during fabrication.

Figs 47A, 47B, 47C, and 47D are top views of a static graphic image. --

Insert the following paragraphs beginning at page 41, line 17:

--Any thin film, medium, or substrate (which can be considered a thick film) can be defined in terms of a characteristic optical admittance. By considering only the reflectance, the operation of a thin film can be studied by treating it as an admittance transformer. That is, a think film or combination of thin films (the transformer) can alter the characteristic admittance of another thin film or substrate (the transformed film) upon which it is deposited. In this fashion a normally reflective film or substrate may have it's characteristic admittance altered (i.e., transformed) in such a way that its reflectivity is enhanced and/or degraded by the deposition of, or contact with, a transformer. In general there is always reflection at the interface between any combination of films, mediums, or substrates. The closer the admittance of the two, the lower

the reflectance at the interface, to the point where the reflectance is zero when the admittances are matched.

Referring to Fig. 36, reflector 3600 (the transformed film) is separated from induced absorber 3605 (the transformer), comprising films 3604, 3606, and 3608, by variable thickness spacer 3602. Incident medium 3610 bounds the other side of induced absorber 3605. Each of these thin films is micromachined in a fashion described in the parent patent application. Induced absorber 3605 performs two functions. The first is to match the admittances of reflector 3600 and incident medium 3610. This is accomplished via matching layer 3608, which is used to transform the admittance of absorber 3606 to that of the incident medium 3610, and via matching layer 3604, which is used to transform the admittance of reflector 3600 to that of absorber 3606. The second function is the absorption of light. This is accomplished using absorber 3606, which performs the function of attenuating light which is incident upon it through the medium, as well as light which is incident upon it from the reflector.

The ability to alter the thickness T of spacer 3602 allows the optical characteristics of the entire structure to be modified. Referring to Fig. 37, pixel 3700 is shown in the driven state and pixel 3702 in the undriven state. In this case induced absorber 3706 (the transformer) resides on substrate 3704 and reflector 3708 (the transformed film) is a self-supporting structure. Application of a voltage causes reflector 3708 to come into contact or close proximity with induced absorber 3706. Proper selection of materials and thickness will result in a complete transformation of the admittance of reflector 3708 to that of substrate 3704. Consequently, a range of frequencies of light 3705, which is incident through substrate 3704, will be significantly absorbed by the pixel. With no voltage applied, reflector 3708 returns to its normal structural state which changes the relative admittances of the reflector and the substrate. In this state (pixel 3702) the cavity behaves more like a resonant reflector, strongly reflecting certain frequencies while strongly absorbing others.

Proper selection of materials thus allows for the fabrication of pixels which can switch from reflecting any color (or combination of colors) to absorbing (e.g., blue to black), or from reflecting any color combination to any other color (e.g., white to red). Referring to Fig. 38, in a specific pixel design, substrate 3802 is glass, matching layer 3804 is a film of zirconium dioxide which is 54.46 nm thick, absorber 3806 is a tungsten film 14.49 nm thick, matching layer 3805 is

a film of silicon dioxide 50 nm thick, spacer 3800 is air, and reflector 3810 is a film of silver at least 50 nm thick. Referring to Fig. 39, the optical response of the pixel is shown in the driven state, i.e., when reflector 3810 is in contact with matching layer 3808 resulting in a broad state of induced absorption. Referring to Figs. 40-43, the different color pixels are shown in respective undriven states which correspond to the reflection of blue, green, red, and white light, respectively. These responses correspond to undriven spacer thicknesses of 325, 435, 230, and 700 nm, respectively.

Referring to Fig. 44, a section of full color reflective flat panel display 4400 includes three kinds of pixels, R, G, and B. Each kind differs from the others only in the size of the undriven spacer which is determined during manufacture as described in the parent patent application. Induced absorber 4402 resides on substrate 4406, and reflector 4410 is self-supporting. Monolithic backplate 4404 provides a hermitic seal and can consist a thick organic or inorganic film. Alternatively, the backplate may consist of a separate piece, such as glass, which has been aligned and bonded to the substrate. Electrodes may reside on this backplate so that the electromechanical performance of the pixels may be modified. Incident light 4412 is transmitted through optical compensation mechanism 4408 and substrate 4406 where it is selectively reflected or absorbed by a pixel. The display may be controlled and driven by circuitry of the kind described in the parent patent application.

Optical compensation mechanism 4408 serves two functions in this display. The first is that of mitigating or eliminating the shift in reflected color with respect to the angle of incidence. This is a characteristic of all interference films and can be compensated for by using films with specifically tailored refractive indices or holographic properties, as well as films containing micro-optics; other ways may also be possible. The second function is to supply a supplemental frontlighting source. In this way, additional light can be added to the front of the display when ambient lighting conditions have significantly diminished thus allowing the display to perform in conditions ranging from intense brightness to total darkness. Such a frontlight could be fabricated using patterned organic emitters or edge lighting source coupled to a micro-optic array within the optical compensation film; other ways may also be possible.

The general process for fabrication of the devices is set forth in the parent patent application. Additional details of two alternative ways to fabricate spacers with different sizes are as follows; other ways may also be possible.

Both alternative processes involve the iterative deposition and patterning of a sacrificial spacer material which, in the final step of the larger process is, etched away to form an air-gap.

Referring to Fig. 45A, substrate 4500 is shown with induced absorber 4502 already deposited and photoresist 4504 deposited and patterned. Induced absorber 4502 is deposited using any number of techniques for think film deposition including sputtering and e-beam deposition. The photoresist is deposited via spinning, and patterned by overexposure to produce a natural overhang resulting in a stencil. The result is that it may be used to pattern subsequently deposited materials using a procedure known as lift-off. Referring to Fig. 45B, spacer material 4506 has been deposited, resulting in excess spacer material 4508 on top of the stencil. Referring to Fig. 45C, the stencil along with the excess spacer material have been lifted off by immersing the device in a bath of solvent such as acetone and agitating it with ultrasound. Referring to Fig. 45D, the process has begun again with new photoresist 4510 having been deposited patterned in a fashion such that new spacer 4512 is deposited adjacent to the old spacer 4506. Repeating the process once more results in spacers with three different thicknesses. Referring to Fig. 45D, the process has begun again with new photoresist 4510 having been deposited patterned in a fashion such that new spacer 4512, with a different thickness, is deposited adjacent to the old spacer 4506.

Referring to Fig. 46A, substrate 4600 is shown with induced absorber 4602 already deposited. Spacer materials 4604, 4606, and 4608 have also been deposited and patterned by virtue of lift-off stencil 4610. The spacer materials have a thickness corresponding to the maximum of the three thicknesses required for the pixels. Referring to Fig. 46B, the stencil along with the excess material has been lifted off and new photoresist 4612 has been deposited and patterned such that spacer 4604 has been left exposed. Referring to Fig. 46C, spacer material 4604 has been etched back via one of a number of techniques which include wet chemical etching, and reactive ion etching. Only a portion of the required spacer material is etched away, with the remainder to be etched in a subsequent etch step. Photoresist 4612 is subsequently removed using a similar technique. Referring to Fig. 46D, new photoresist 4614

has been deposited and patterned exposing spacers 4604 and 4606. The entire etch of spacer 4606 is performed in this step, and the etch of spacer 4604 is completed. Photoresist 4614 is subsequently removed and the process is complete.

Other embodiments are within the scope of the following claims.

For example, the spacer material need not ultimately be etched away but may remain instead a part of the finished device. In this fashion, and using the previously described patterning techniques, arbitrary patterns may be fabricated instead of arrays of simple pixels. Full color static graphical images may thus be rendered in a method which is analogous to a conventional printing process. In conventional printing, an image is broken up into color separations which are basically monochrome graphical subsets of the image, which correspond to the different colors represented, i.e., a red separation, a blue separation, a green separation, and a black separation. The full-color image is produced by printing each separation using a different colored ink on the same area.

Alternatively, in a process which we will call "Iridescent Printing", the different separations are composed of layers of thin films which correspond to the IMod design described here and those in the referenced patent. Patterning or printing a combination of colors or separations on the same area, allows for brilliant full-color images to be produced.

Referring to Fig. 47A, a square substrate is shown with area 4700 representing the portion of the substrate which has been patterned with a thin film stack optimized for black. Referring to Fig. 47B, the substrate has been subsequently patterned with a thin film stack optimized for red in area 4702. Referring to Fig. 47C, the substrate has been subsequently patterned with a thin film stack optimized for green in area 4704. Referring to Fig. 47D, the substrate has been subsequently patterned with a think film stack optimized for blue in area 4706.

Alternatively, a simpler process can be obtained if only the induced absorber design is used. In this process, the entire substrate is first coated with the induced absorber stack. Subsequent steps are then used to pattern the spacer material only, using the aforementioned techniques. After the desired spacers, i.e., colors are defined, a final deposition of a reflector is performed.

The brightness of different colors can be altered by varying the amount of black interspersed with the particular color, i.e., spatial dithering. The images also exhibit the pleasing shift of color with respect to viewing angle known as iridescence.

In another example, a reflective flat panel display may also be fabricated using a single kind of pixel instead of three. Multiple colors, in this case, are obtained through fabricating the pixels in the form of continuously tunable or analog interferometric modulators as described in the parent patent application. In this fashion, any individual pixel may, by the application of the appropriate voltage, be tuned to reflect any specific color. This would require that the array be fabricated on a substrate along with electronic circuitry, or directly on the surface of an integrated circuit, in order to provide a charge storage mechanism. This approach, though it requires a more complicated driving scheme relying on analog voltages, provides superior resolution. It would also find application in a projection system. --

<u>In the claims</u>:

Cancel claims 1-9 and 12-39.

<u>In the drawings</u>:

Insert the following new figures after Figure 35:

Figures 36-44, 45A-45D, 46A-046D, and 47A-47D.

REMARKS

Applicant asks that all claims be examined. Please apply any other charges or credits to Deposit Account No. 06-1050, reference 01568-006004.

Respectfully submitted,

Date: 2/19/2

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Version with markings to show changes made

In the specification:

Paragraph beginning at page 1, line 5 has been amended as follows:

This application is a divisional and claims priority from U.S. application 08/769,947, filed December 12, 1996, [This is a continuation in part of United States Patent Applications Serial Numbers 08/238,750, and 08/554,630, filed May 5, 1994, and November 5, 1995, respectively, and incorporated by reference.] which is a continuation-in-part of both U.S. application No. 08/238,750, filed May 5, 1994, now issued as U.S. Patent No. 5,835,255, (incorporated herein by reference) and U.S. application No. 08/554,630, filed November 6, 1995, now abandoned.

Insert the following paragraph beginning at page 1, line 23:

One such design includes a filter described as a hybrid filter which has a narrow bandpass filter and an induced absorber. When the wall associated with the hybrid filter is brought into contact with a reflector, incident light of a certain range is absorbed. This occurs because the induced absorber matches the impedance of the reflector to that of the incident medium for the range of frequencies passed by the narrow-band filter.

Insert the following paragraphs beginning at page 12, line 32:

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Referring to Fig. 36, reflector 3600 (the transformed film) is separated from induced absorber 3605 (the transformer), comprising films 3604, 3606, and 3608, by variable thickness spacer 3602. Incident medium 3610 bounds the other side of induced absorber 3605. Each of these thin films is micromachined in a fashion described in the parent patent application. Induced absorber 3605 performs two functions. The first is to match the admittances of reflector 3600 and incident medium 3610. This is accomplished via matching layer 3608, which is used to transform the admittance of absorber 3606 to that of the incident medium 3610, and via matching layer 3604, which is used to transform the admittance of reflector 3600 to that of absorber 3606. The second function is the absorption of light. This is accomplished using absorber 3606, which performs the function of attenuating light which is incident upon it through the medium, as well as light which is incident upon it from the reflector.

The ability to alter the thickness T of spacer 3602 allows the optical characteristics of the entire structure to be modified. Referring to Fig. 37, pixel 3700 is shown in the driven state and pixel 3702 in the undriven state. In this case induced absorber 3706 (the transformer) resides on substrate 3704 and reflector 3708 (the transformed film) is a self-supporting structure. Application of a voltage causes reflector 3708 to come into contact or close proximity with induced absorber 3706. Proper selection of materials and thickness will result in a complete transformation of the admittance of reflector 3708 to that of substrate 3704. Consequently, a range of frequencies of light 3705, which is incident through substrate 3704, will be significantly absorbed by the pixel. With no voltage applied, reflector 3708 returns to its normal structural state which changes the relative admittances of the reflector and the substrate. In this state (pixel 3702) the cavity behaves more like a resonant reflector, strongly reflecting certain frequencies while strongly absorbing others.

Proper selection of materials thus allows for the fabrication of pixels which can switch from reflecting any color (or combination of colors) to absorbing (e.g., blue to black), or from reflecting any color combination to any other color (e.g., white to red). Referring to Fig. 38, in a specific pixel design, substrate 3802 is glass, matching layer 3804 is a film of zirconium dioxide which is 54.46 nm thick, absorber 3806 is a tungsten film 14.49 nm thick, matching layer 3805 is a film of silicon dioxide 50 nm thick, spacer 3800 is air, and reflector 3810 is a film of silver at least 50 nm thick. Referring to Fig. 39, the optical response of the pixel is shown in the driven state, i.e., when reflector 3810 is in contact with matching layer 3808 resulting in a broad state of induced absorption. Referring to Figs. 40-43, the different color pixels are shown in respective undriven states which correspond to the reflection of blue, green, red, and white light, respectively. These responses correspond to undriven spacer thicknesses of 325, 435, 230, and 700 nm, respectively.

Referring to Fig. 44, a section of full color reflective flat panel display 4400 includes three kinds of pixels, R, G, and B. Each kind differs from the others only in the size of the undriven spacer which is determined during manufacture as described in the parent patent application. Induced absorber 4402 resides on substrate 4406, and reflector 4410 is self-supporting. Monolithic backplate 4404 provides a hermitic seal and can consist a thick organic or inorganic film. Alternatively, the backplate may consist of a separate piece, such as glass,

which has been aligned and bonded to the substrate. Electrodes may reside on this backplate so that the electromechanical performance of the pixels may be modified. Incident light 4412 is transmitted through optical compensation mechanism 4408 and substrate 4406 where it is selectively reflected or absorbed by a pixel. The display may be controlled and driven by circuitry of the kind described in the parent patent application.

Optical compensation mechanism 4408 serves two functions in this display. The first is that of mitigating or eliminating the shift in reflected color with respect to the angle of incidence. This is a characteristic of all interference films and can be compensated for by using films with specifically tailored refractive indices or holographic properties, as well as films containing micro-optics; other ways may also be possible. The second function is to supply a supplemental frontlighting source. In this way, additional light can be added to the front of the display when ambient lighting conditions have significantly diminished thus allowing the display to perform in conditions ranging from intense brightness to total darkness. Such a frontlight could be fabricated using patterned organic emitters or edge lighting source coupled to a micro-optic array within the optical compensation film; other ways may also be possible.

The general process for fabrication of the devices is set forth in the parent patent application. Additional details of two alternative ways to fabricate spacers with different sizes are as follows; other ways may also be possible.

Both alternative processes involve the iterative deposition and patterning of a sacrificial spacer material which, in the final step of the larger process is, etched away to form an air-gap.

Referring to Fig. 45A, substrate 4500 is shown with induced absorber 4502 already deposited and photoresist 4504 deposited and patterned. Induced absorber 4502 is deposited using any number of techniques for think film deposition including sputtering and e-beam deposition. The photoresist is deposited via spinning, and patterned by overexposure to produce a natural overhang resulting in a stencil. The result is that it may be used to pattern subsequently deposited materials using a procedure known as lift-off. Referring to Fig. 45B, spacer material 4506 has been deposited, resulting in excess spacer material 4508 on top of the stencil. Referring to Fig. 45C, the stencil along with the excess spacer material have been lifted off by immersing the device in a bath of solvent such as acetone and agitating it with ultrasound. Referring to Fig. 45D, the process has begun again with new photoresist 4510 having been

deposited patterned in a fashion such that new spacer 4512 is deposited adjacent to the old spacer 4506. Repeating the process once more results in spacers with three different thicknesses. Referring to Fig. 45D, the process has begun again with new photoresist 4510 having been deposited patterned in a fashion such that new spacer 4512, with a different thickness, is deposited adjacent to the old spacer 4506.

Referring to Fig. 46A, substrate 4600 is shown with induced absorber 4602 already deposited. Spacer materials 4604, 4606, and 4608 have also been deposited and patterned by virtue of lift-off stencil 4610. The spacer materials have a thickness corresponding to the maximum of the three thicknesses required for the pixels. Referring to Fig. 46B, the stencil along with the excess material has been lifted off and new photoresist 4612 has been deposited and patterned such that spacer 4604 has been left exposed. Referring to Fig. 46C, spacer material 4604 has been etched back via one of a number of techniques which include wet chemical etching, and reactive ion etching. Only a portion of the required spacer material is etched away, with the remainder to be etched in a subsequent etch step. Photoresist 4612 is subsequently removed using a similar technique. Referring to Fig. 46D, new photoresist 4614 has been deposited and patterned exposing spacers 4604 and 4606. The entire etch of spacer 4606 is performed in this step, and the etch of spacer 4604 is completed. Photoresist 4614 is subsequently removed and the process is complete.

Other embodiments are within the scope of the following claims.

For example, the spacer material need not ultimately be etched away but may remain instead a part of the finished device. In this fashion, and using the previously described patterning techniques, arbitrary patterns may be fabricated instead of arrays of simple pixels. Full color static graphical images may thus be rendered in a method which is analogous to a conventional printing process. In conventional printing, an image is broken up into color separations which are basically monochrome graphical subsets of the image, which correspond to the different colors represented, i.e., a red separation, a blue separation, a green separation, and a black separation. The full-color image is produced by printing each separation using a different colored ink on the same area.

Alternatively, in a process which we will call "Iridescent Printing", the different separations are composed of layers of thin films which correspond to the IMod design described

here and those in the referenced patent. Patterning or printing a combination of colors or separations on the same area, allows for brilliant full-color images to be produced.

Referring to Fig. 47A, a square substrate is shown with area 4700 representing the portion of the substrate which has been patterned with a thin film stack optimized for black. Referring to Fig. 47B, the substrate has been subsequently patterned with a thin film stack optimized for red in area 4702. Referring to Fig. 47C, the substrate has been subsequently patterned with a thin film stack optimized for green in area 4704. Referring to Fig. 47D, the substrate has been subsequently patterned with a think film stack optimized for blue in area 4706.

Alternatively, a simpler process can be obtained if only the induced absorber design is used. In this process, the entire substrate is first coated with the induced absorber stack. Subsequent steps are then used to pattern the spacer material only, using the aforementioned techniques. After the desired spacers, i.e., colors are defined, a final deposition of a reflector is performed.

The brightness of different colors can be altered by varying the amount of black interspersed with the particular color, i.e., spatial dithering. The images also exhibit the pleasing shift of color with respect to viewing angle known as iridescence.

In another example, a reflective flat panel display may also be fabricated using a single kind of pixel instead of three. Multiple colors, in this case, are obtained through fabricating the pixels in the form of continuously tunable or analog interferometric modulators as described in the parent patent application. In this fashion, any individual pixel may, by the application of the appropriate voltage, be tuned to reflect any specific color. This would require that the array be fabricated on a substrate along with electronic circuitry, or directly on the surface of an integrated circuit, in order to provide a charge storage mechanism. This approach, though it requires a more complicated driving scheme relying on analog voltages, provides superior resolution. It would also find application in a projection system.

In the claims:

Claims 1-9 and 12-39 have been cancelled.

Fig. 1A

Fig. 2

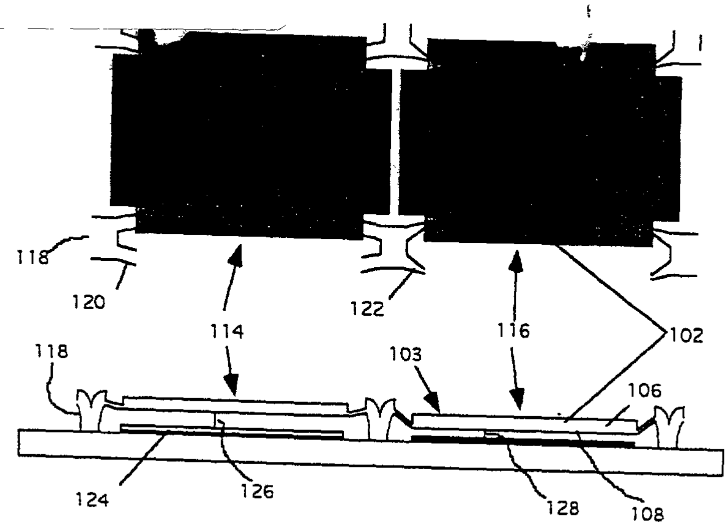


Fig. 1B

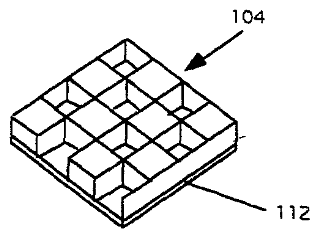


Fig. 3A

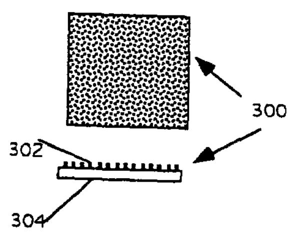


Fig. 3B

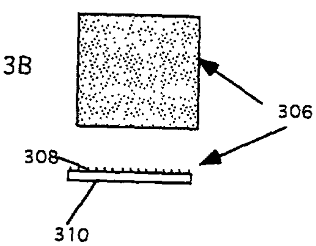


Fig. 4

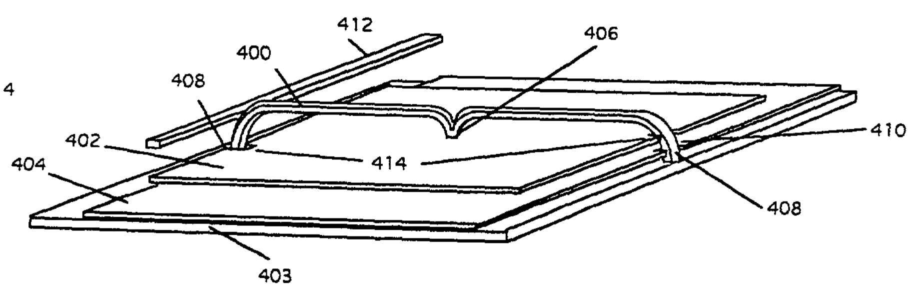


Fig. 5A

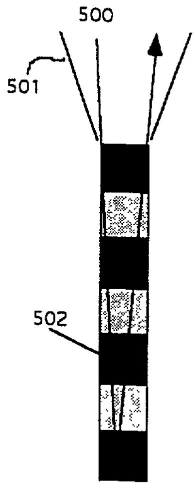


Fig. 5B

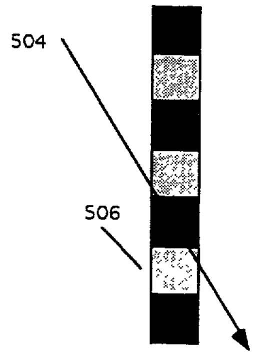


Fig. 5C

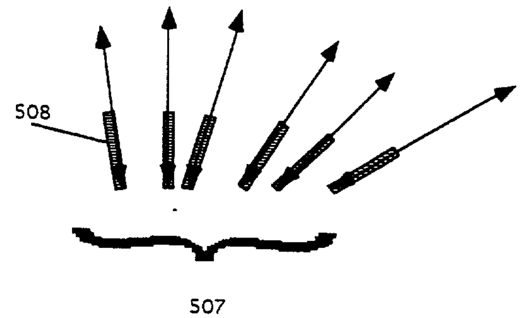


Fig. 6A

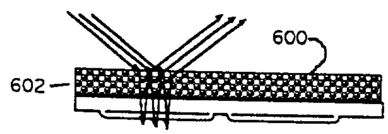


Fig. 6B

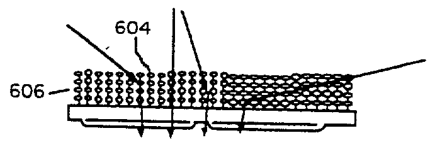


Fig. 6C

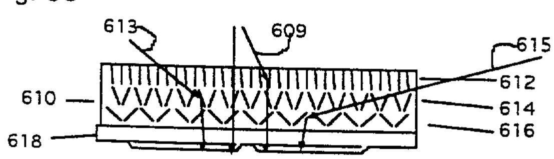


Fig. 6D

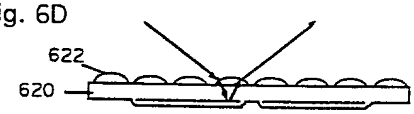


Fig. 6E

Fig. 6F

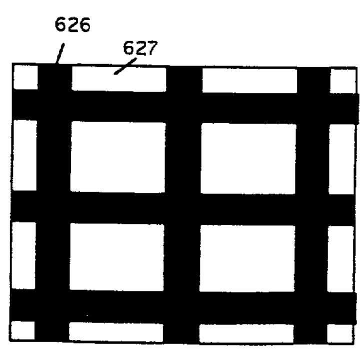


Fig. 7A

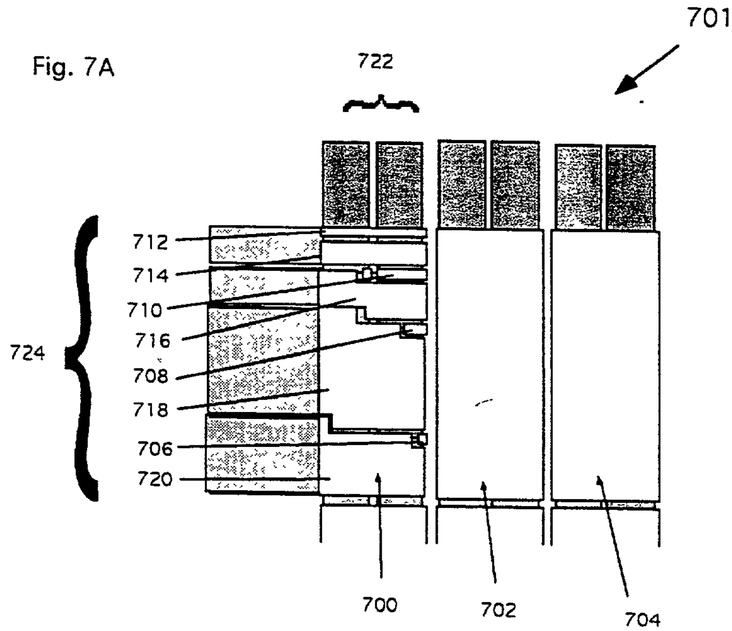


Fig. 7B

Fig. 8

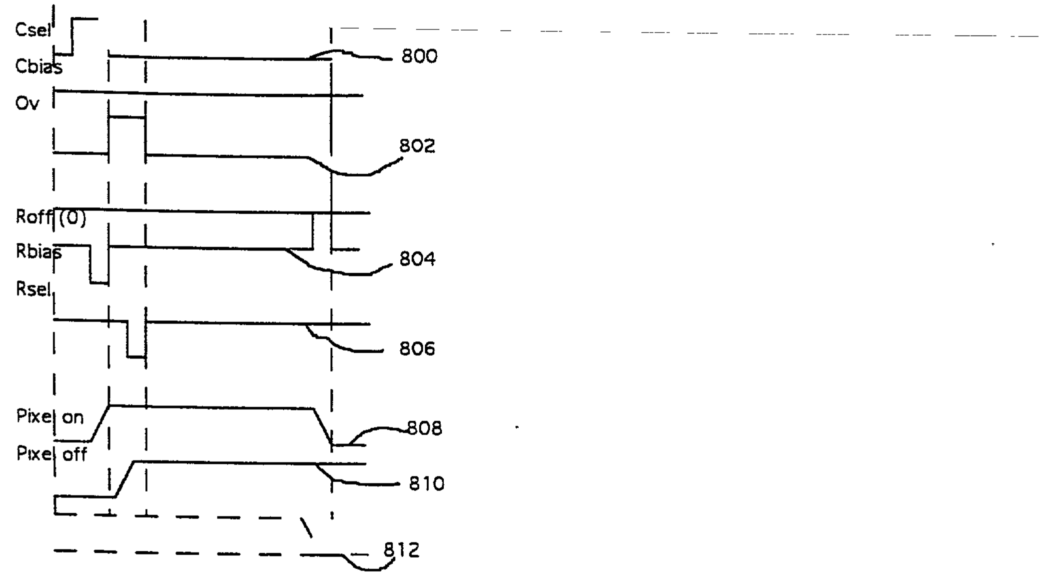


Fig. 9

$C_{sel} = V_0 + (V_3 - V_2)$
$C_{bias} = 1/2(V_1) = V_0$
$R_{bias} = -(V_1 - V_0)$
$R_{sel} = -(V_2 - V_0)$

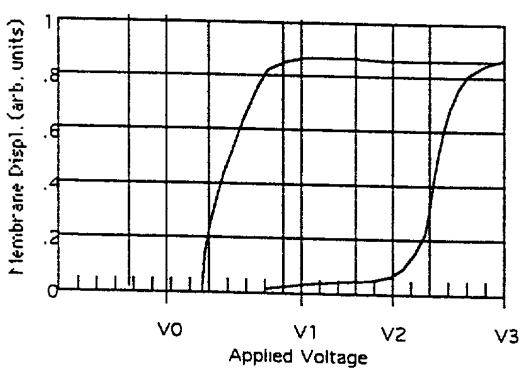


Fig. 10A

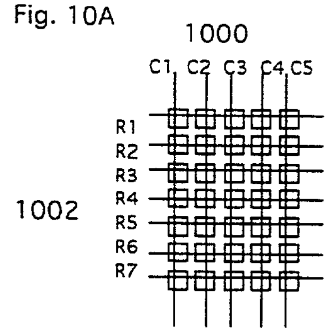


Fig. 10B

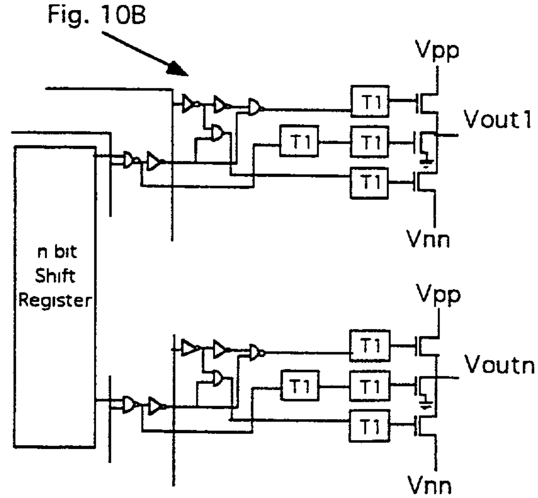


Fig. 11

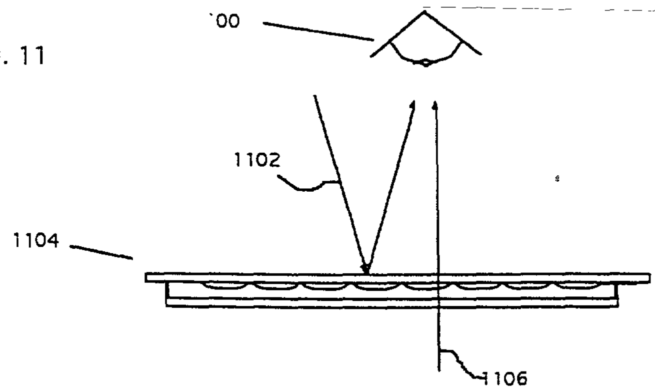


Fig. 12

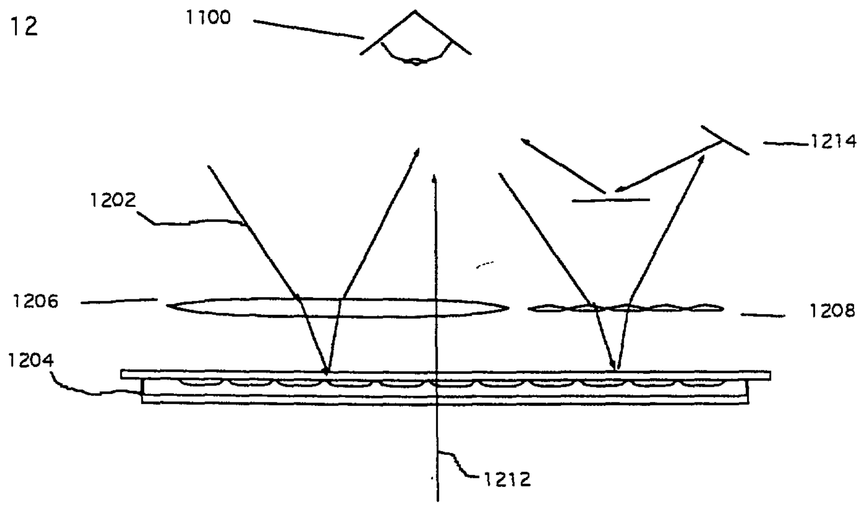


Fig. 13

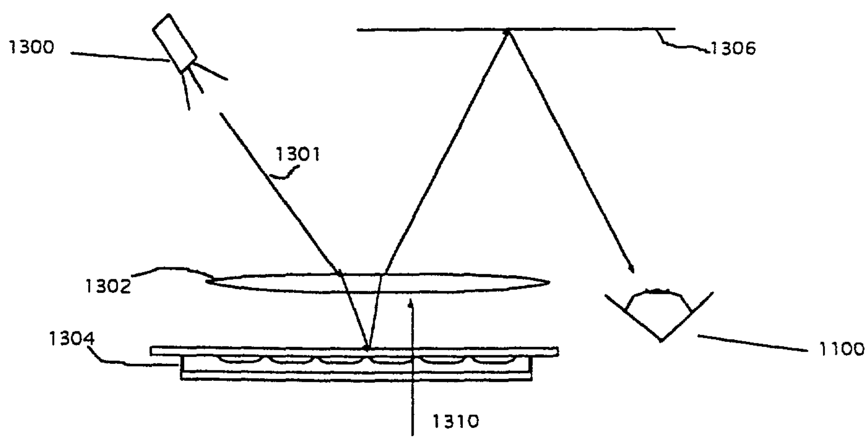


Fig. 14

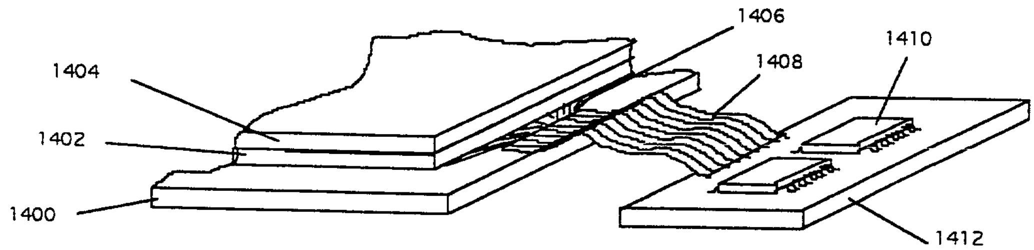


Fig. 15

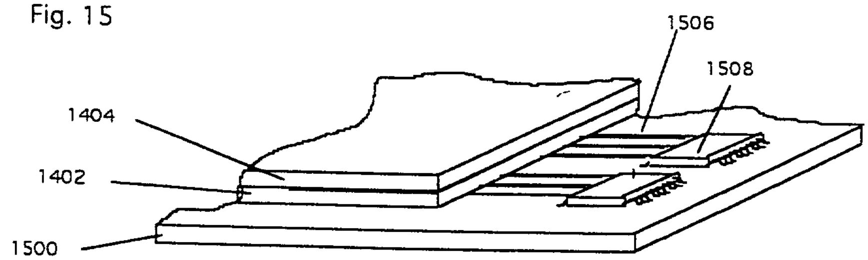


Fig. 16

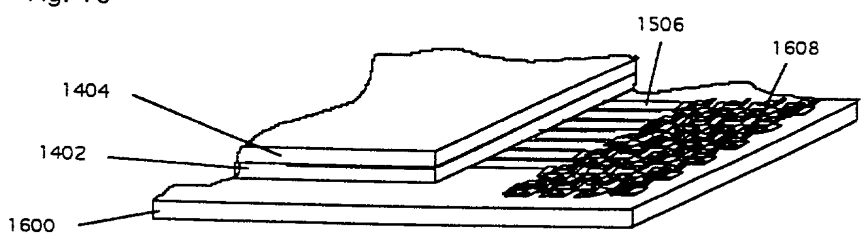


Fig. 17A

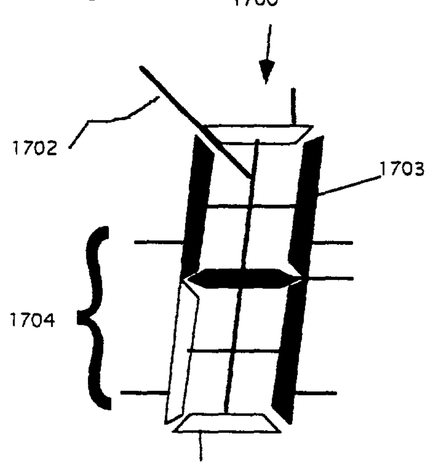


Fig. 17B

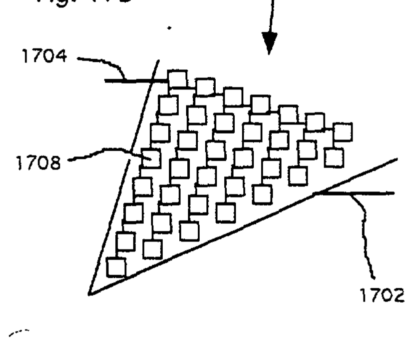


Fig. 18A

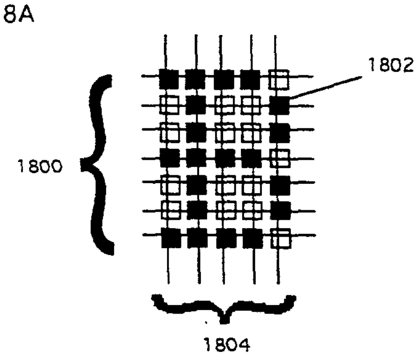


Fig. 18B

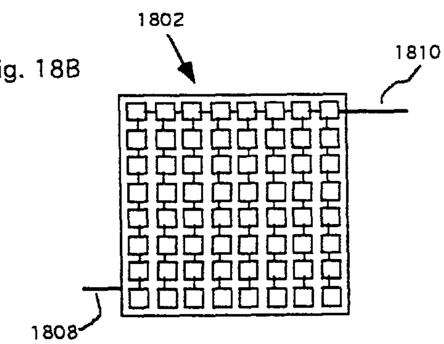


Fig. 19

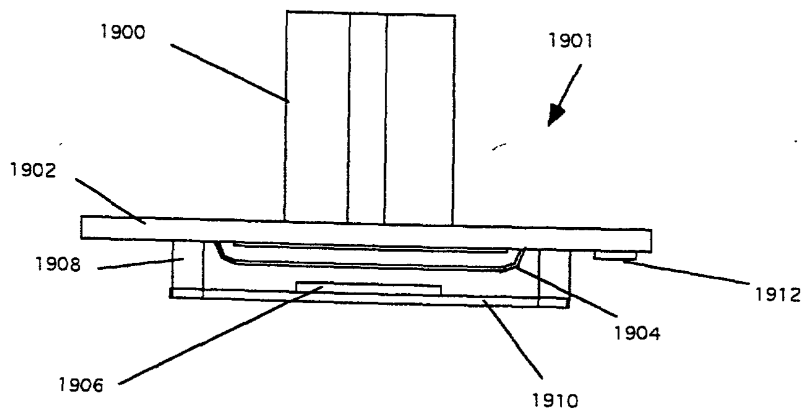


Fig. 20

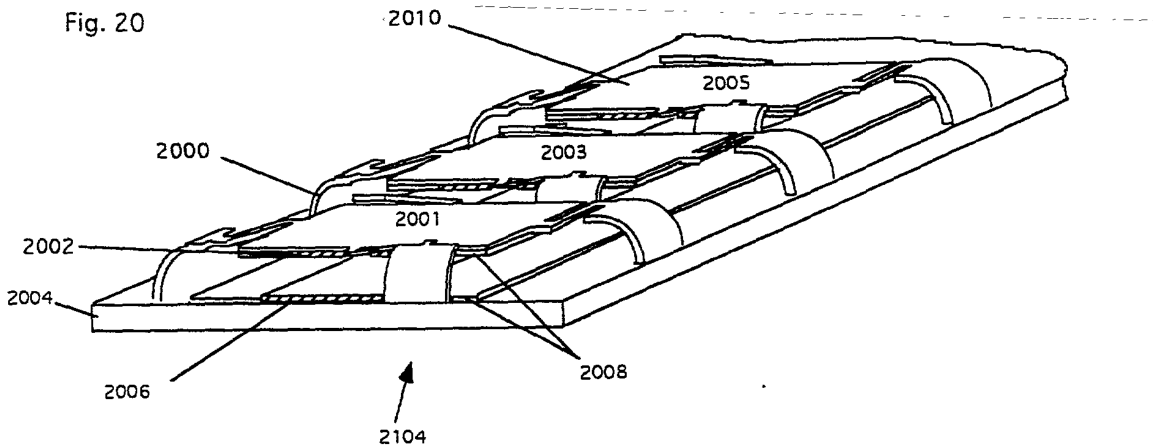


Fig. 21A

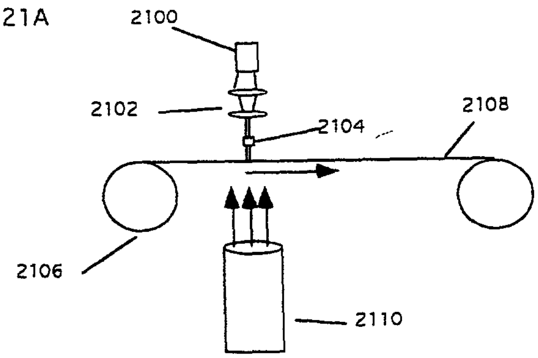


Fig. 21B

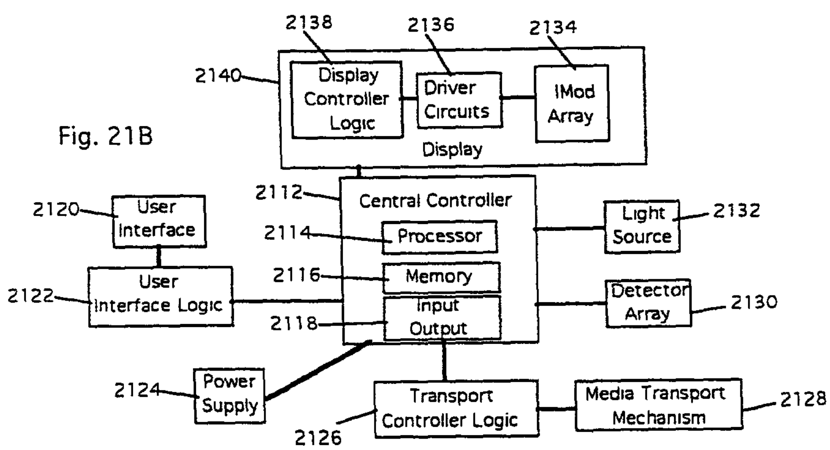


Fig. 22

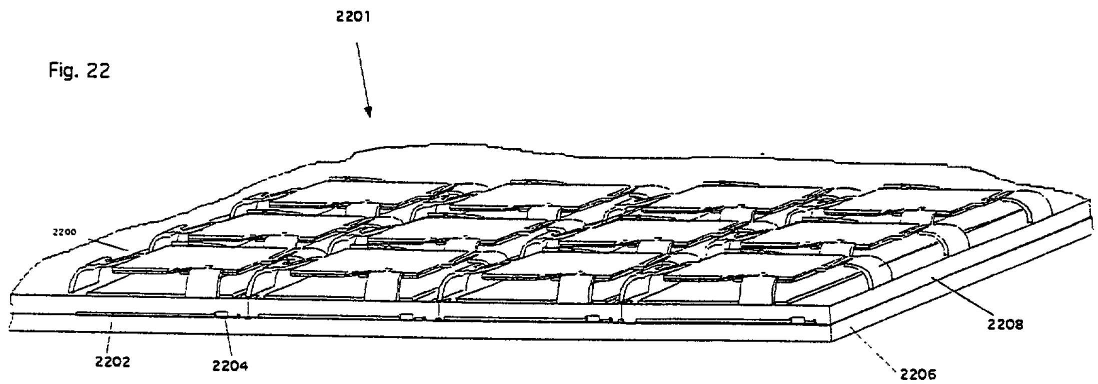


Fig. 23.

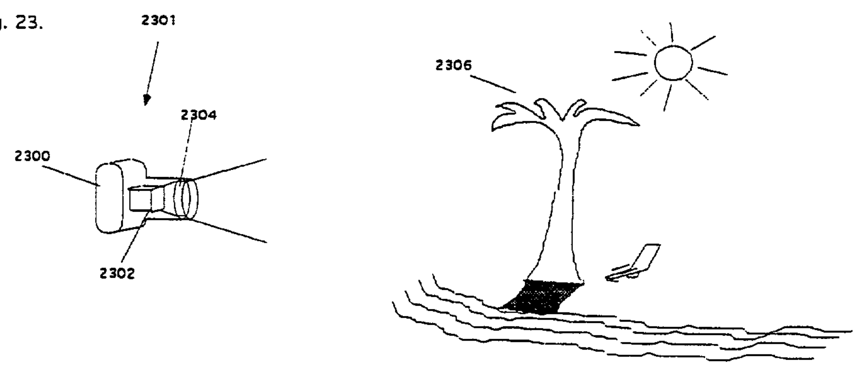


Fig. 24A

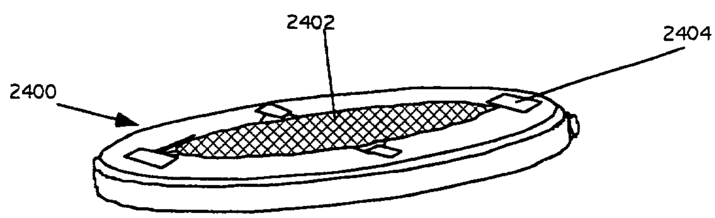


Fig. 24B

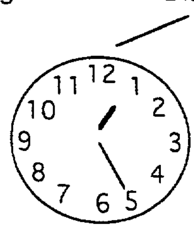


Fig. 24C

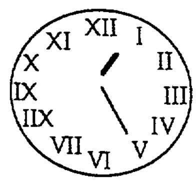


Fig. 24D

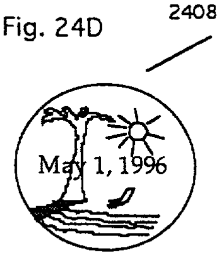


Fig. 24E

Fig. 25A

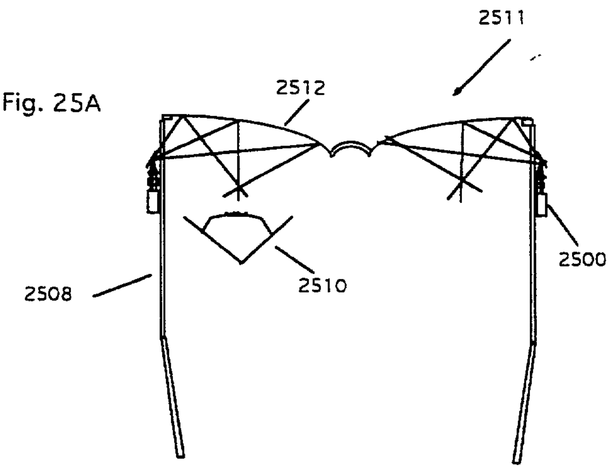


Fig. 25B

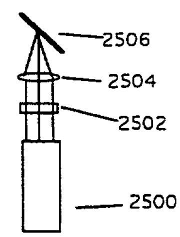


Fig. 26A

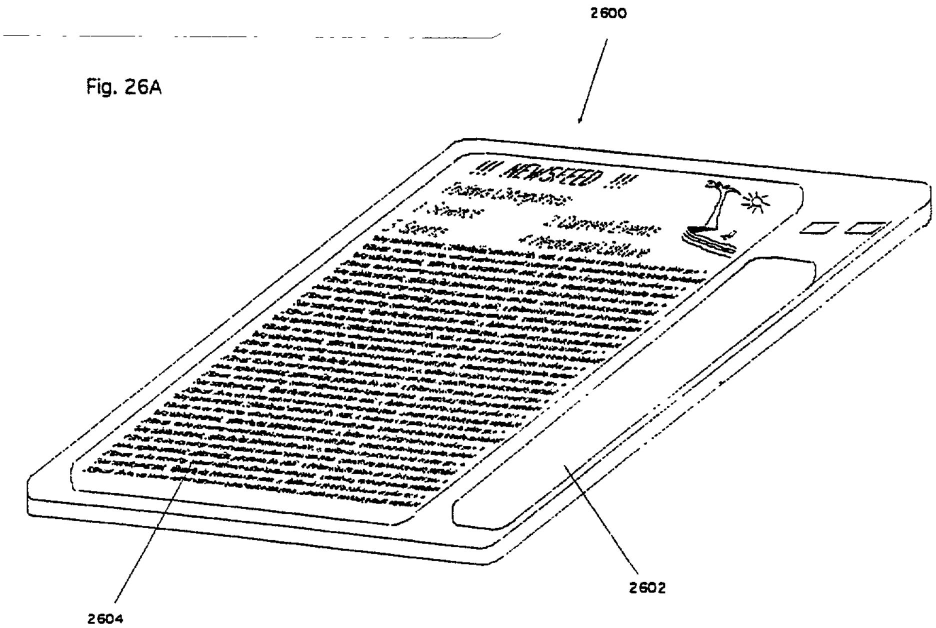


Fig. 26B

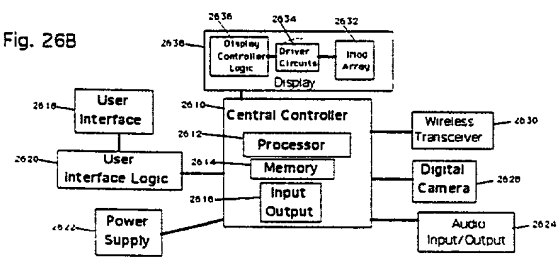


Fig. 26C

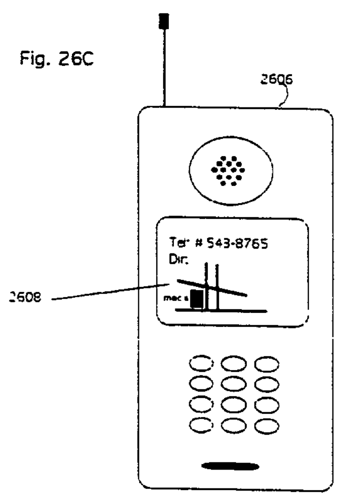


Fig. 26D

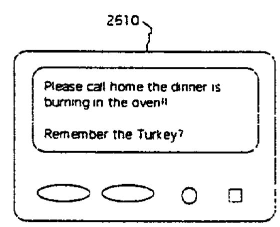
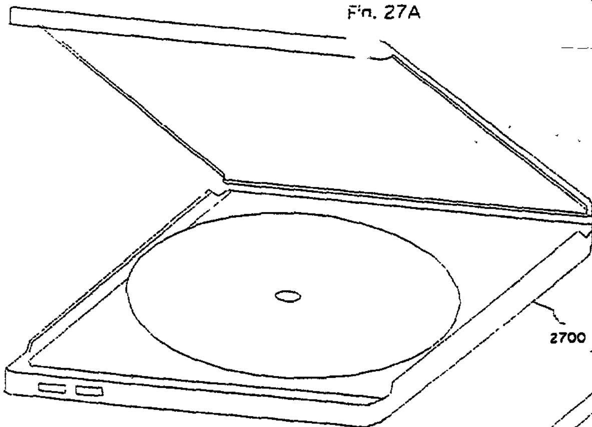


Fig. 27A



2700

Fig. 27B

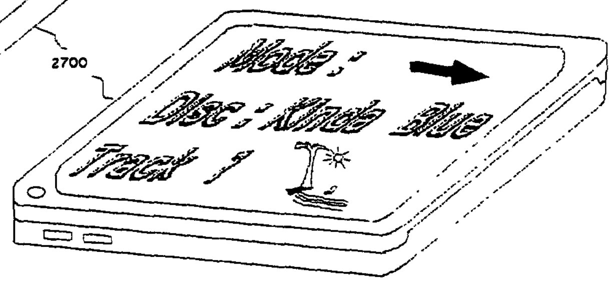


Fig. 27C

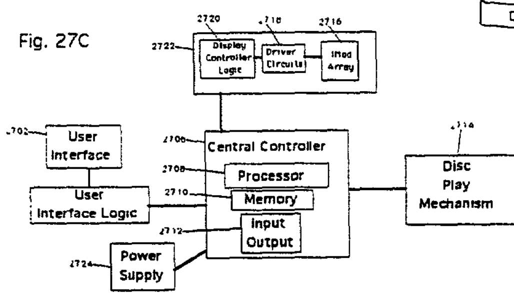
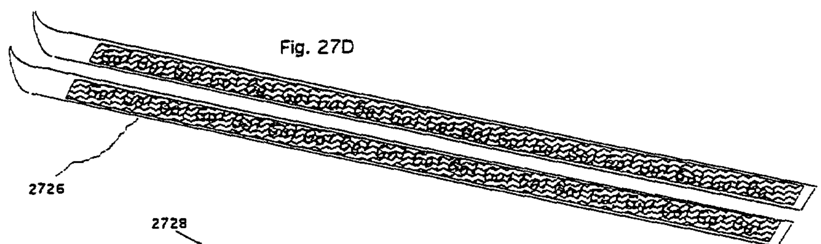


Fig. 27D



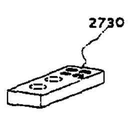


Fig. 27E

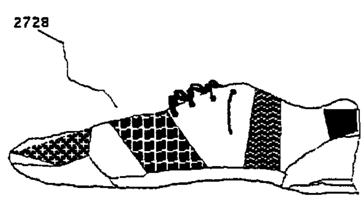


Fig. 27F

Fig. 27G

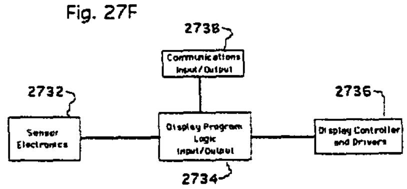


Fig. 28A

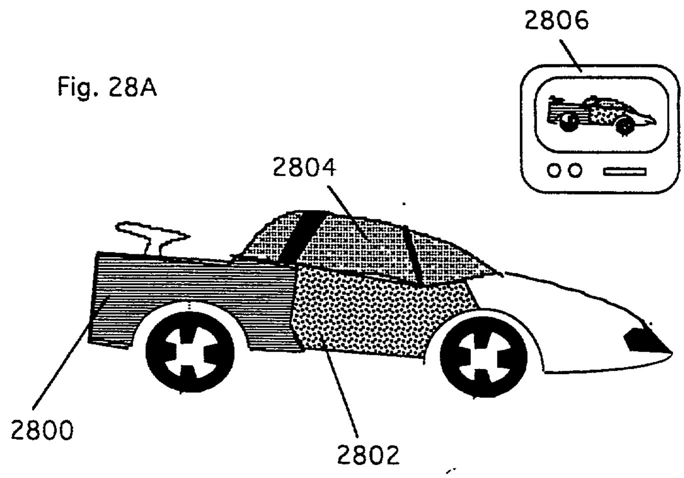
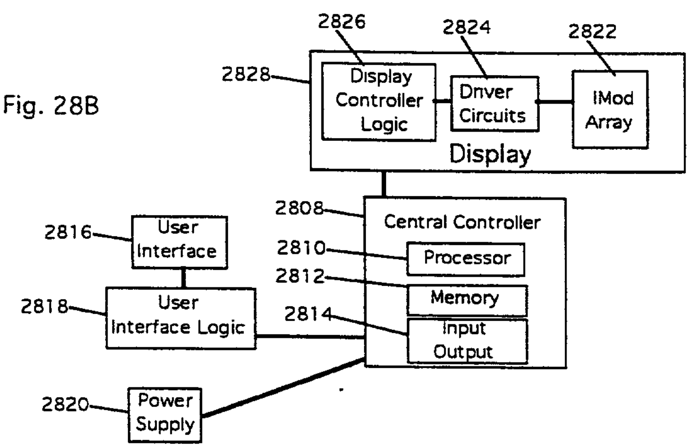


Fig. 28B



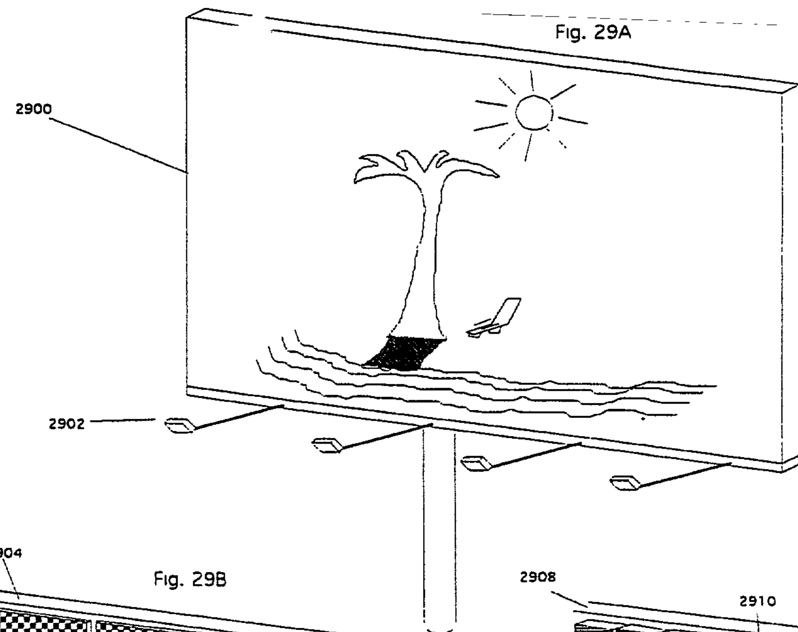


Fig. 29A

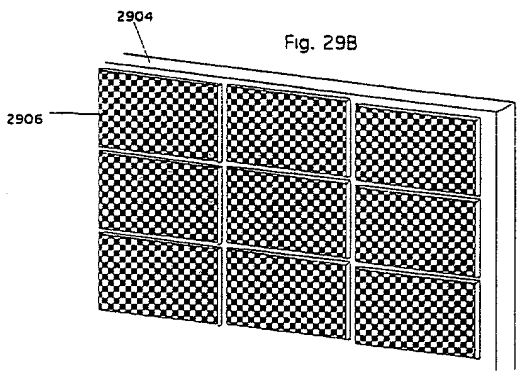


Fig. 29B

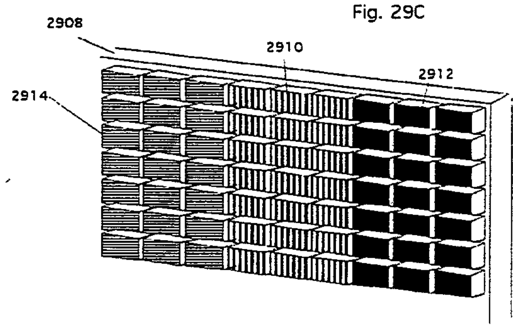


Fig. 29C

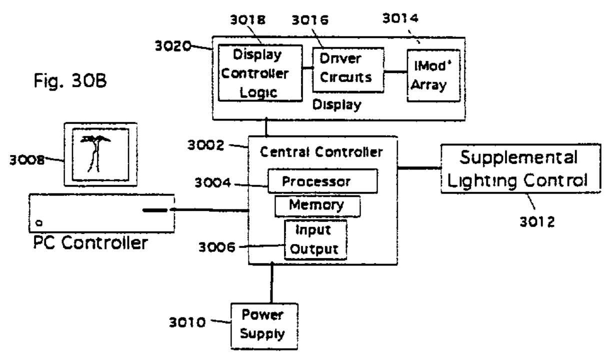


Fig. 30B

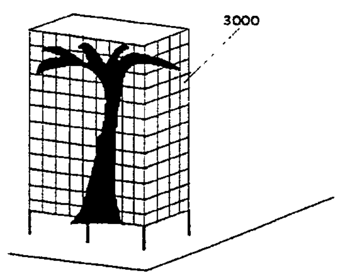


Fig. 30A

Fig. 31A

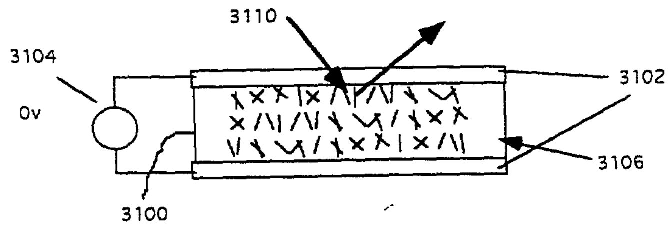


Fig. 31B

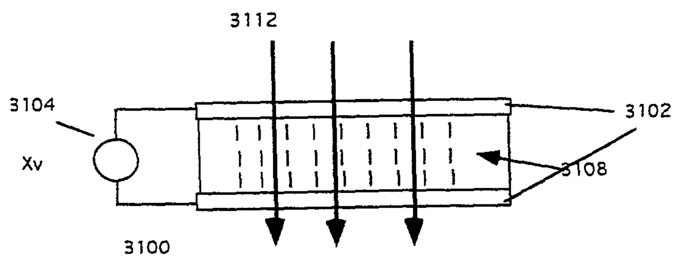


Fig. 32A

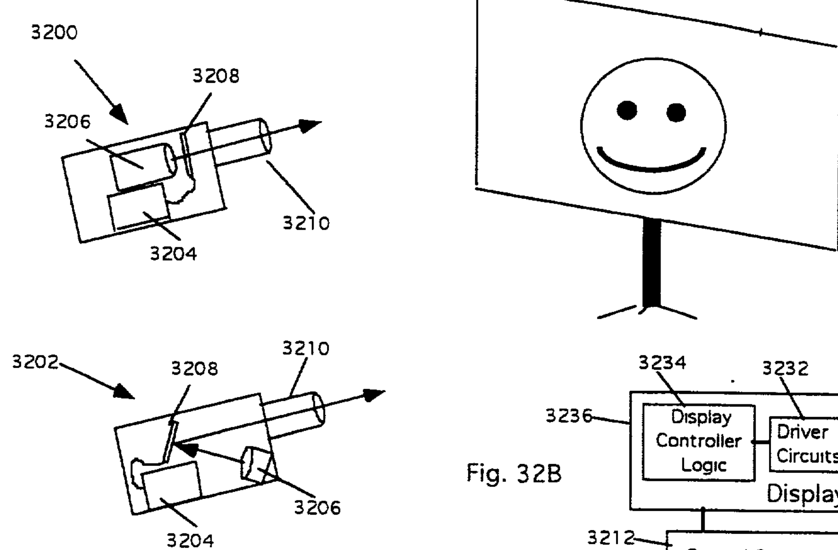


Fig. 32B

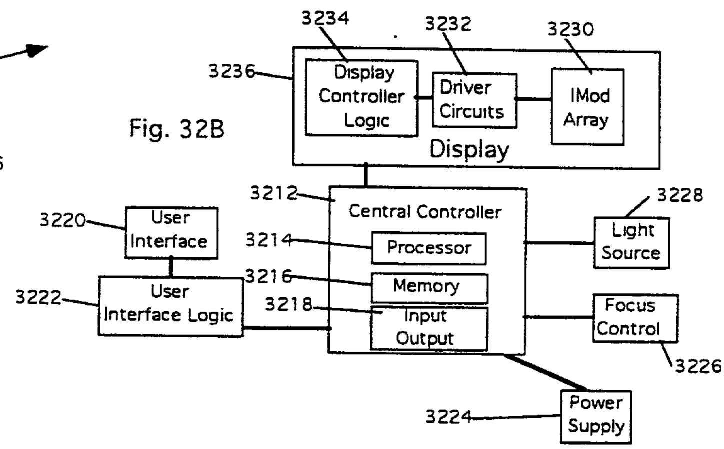


Fig. 33A

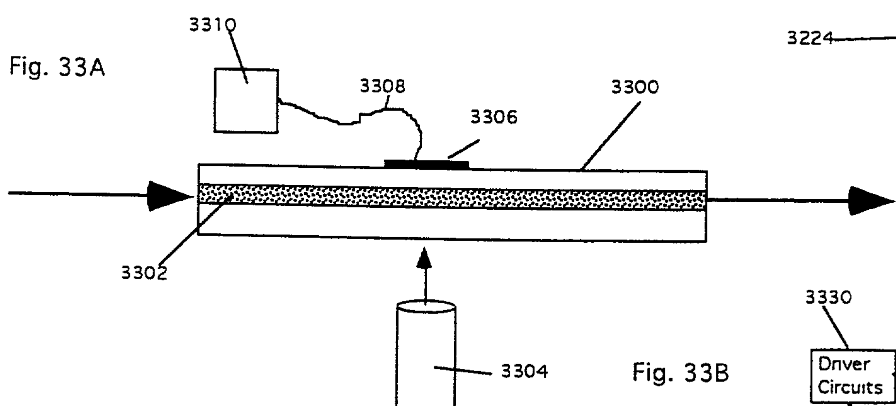


Fig. 33B

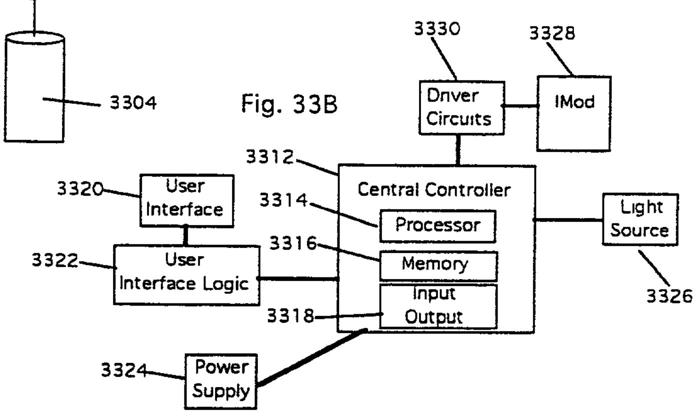


Fig. 34A

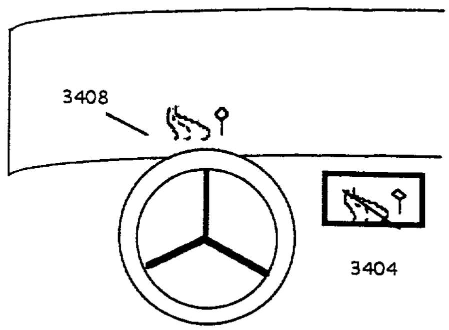


Fig. 34B

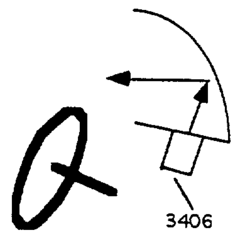


Fig. 34C

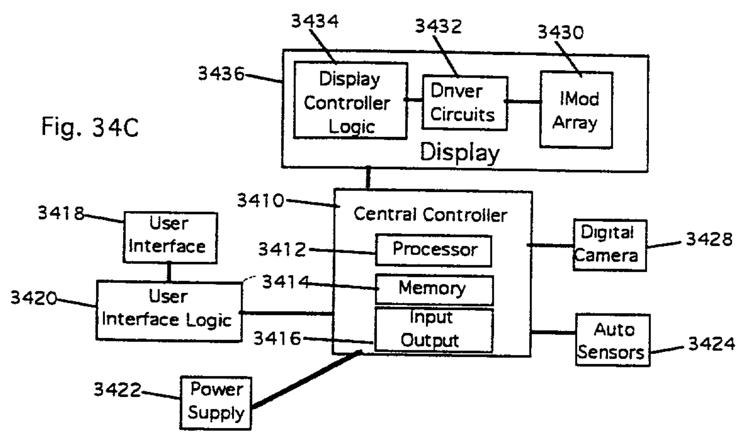


Fig. 35A

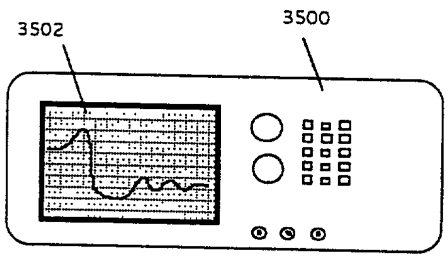
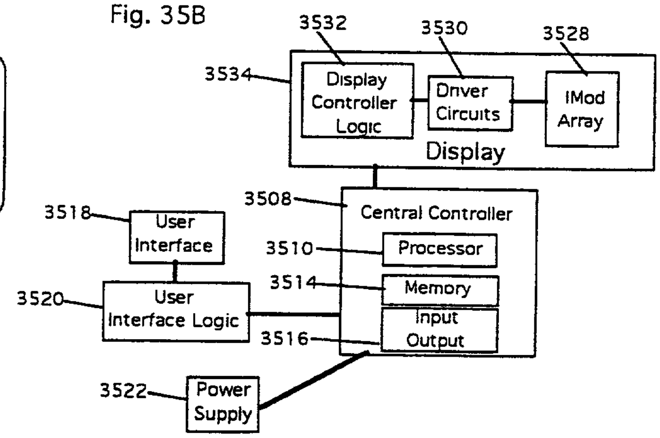


Fig. 35B



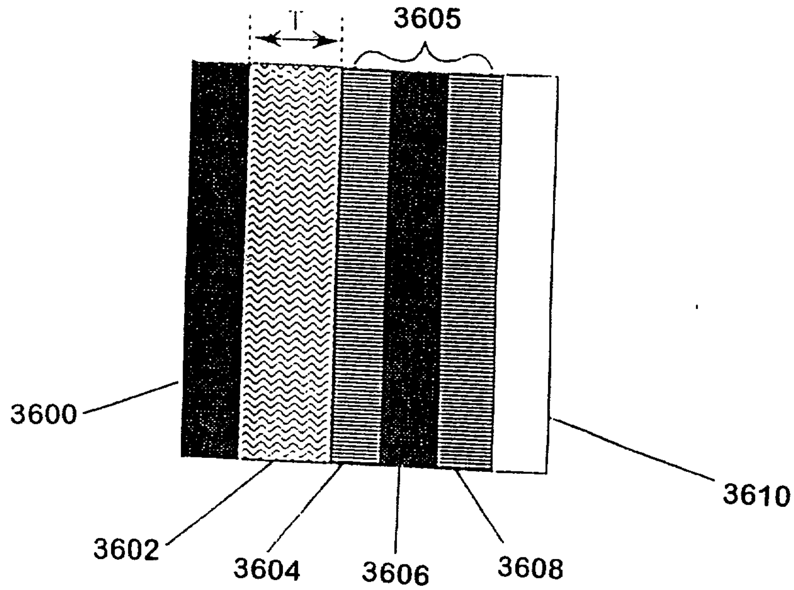


FIG.36

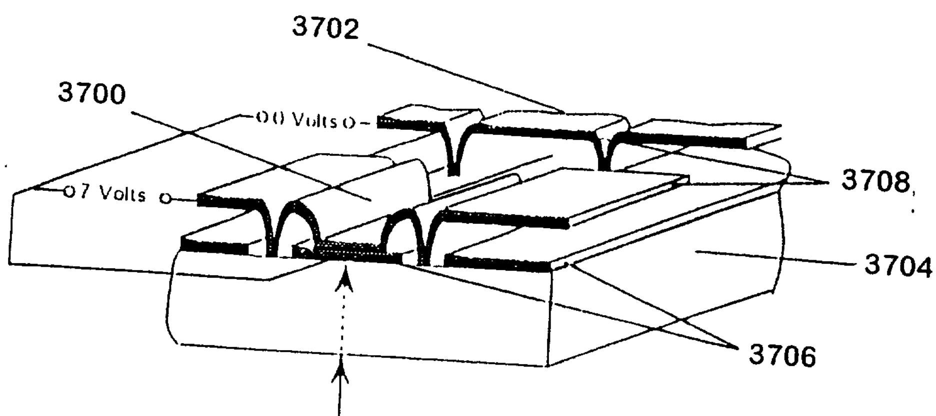


FIG.37

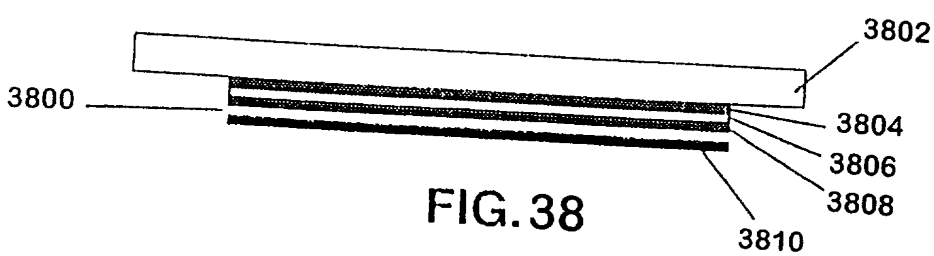


FIG.38

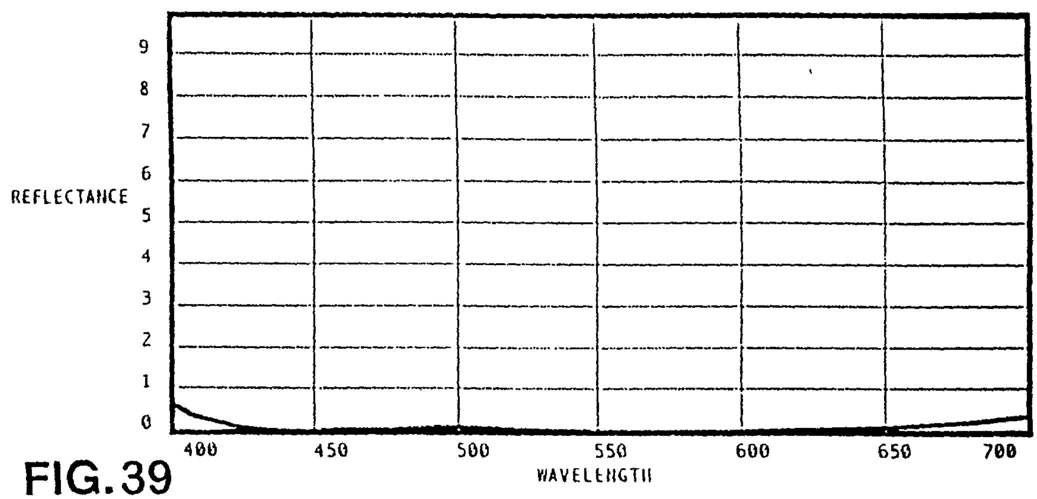
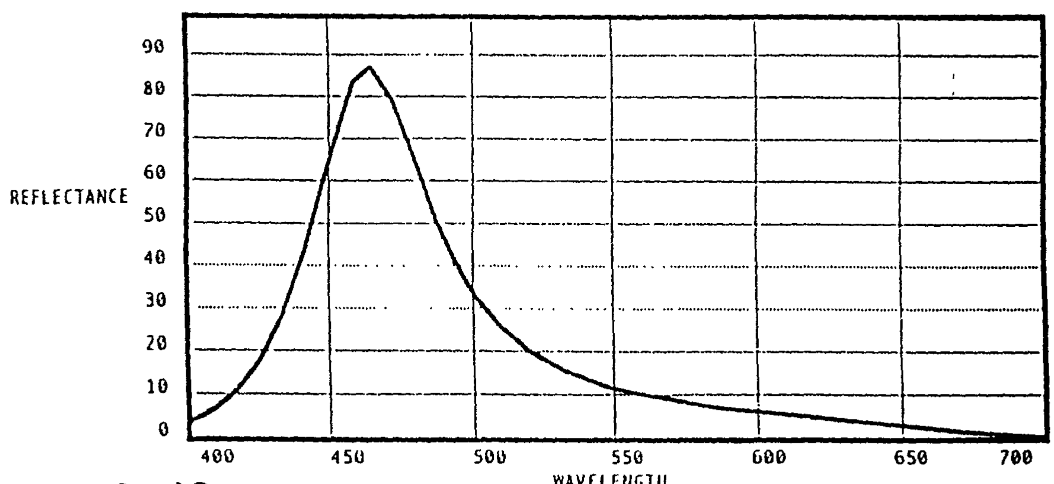

REFLECTANCE
9
8
7
6
5
4
3
2
1
0
400
450
500
550
600
650
700
WAVELENGTH

FIG.39


REFLECTANCE
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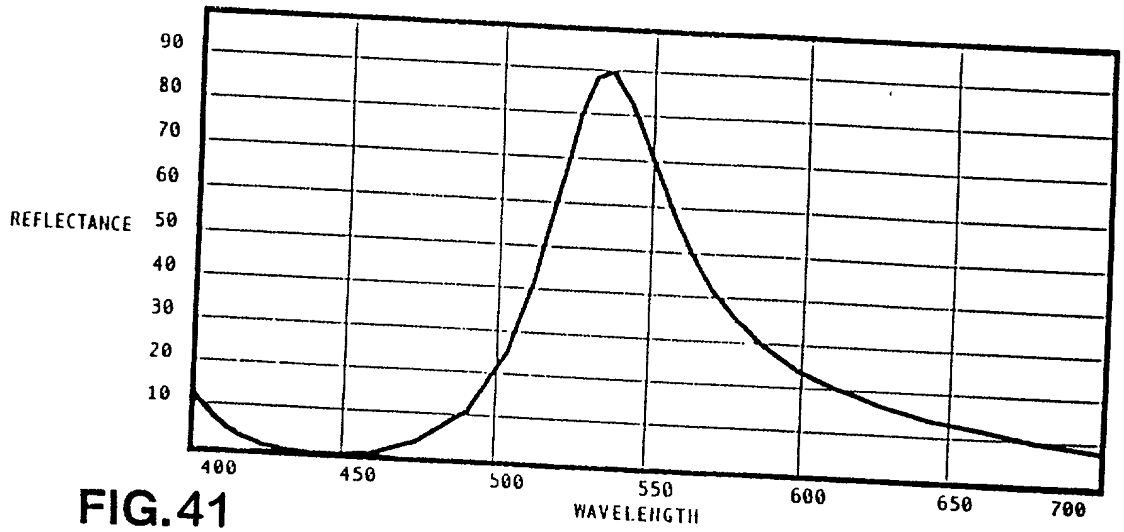
80
70
60
50
40
30
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG.40


90
80
70
60
REFLECTANCE
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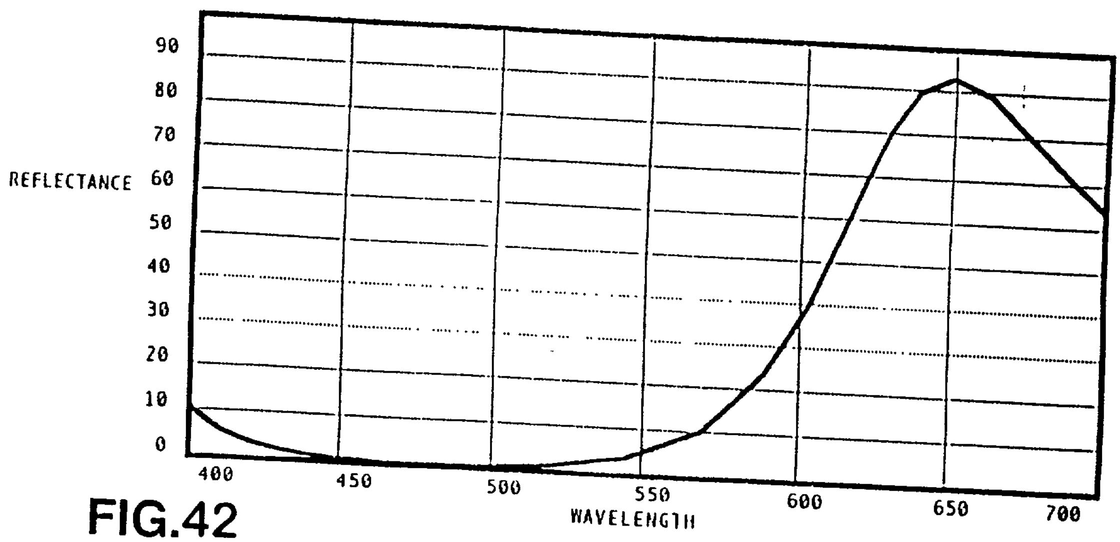
40
30
20
10
400
450
500
550
600
650
700
WAVELENGTH

FIG.41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REFLECTANCE
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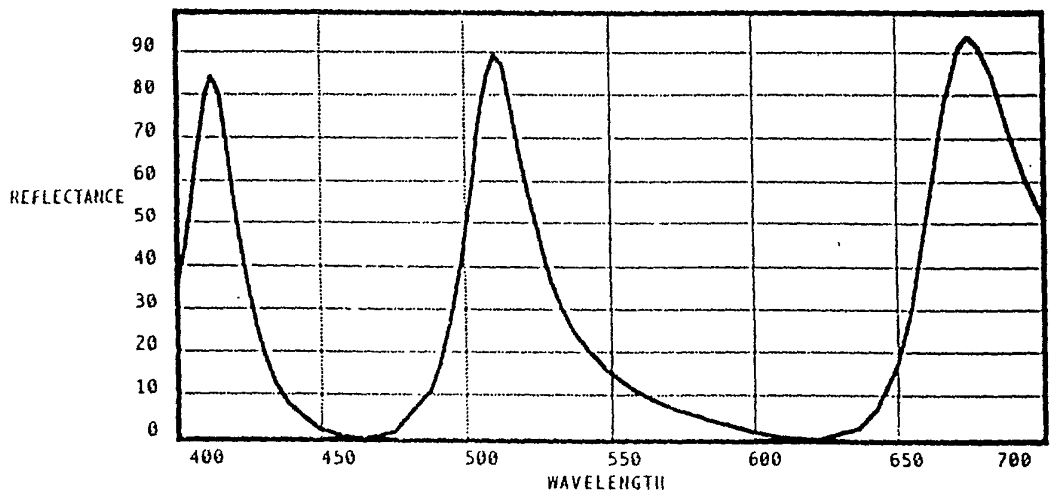
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG.42


REFLECTANCE
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80
70
60
50
40
30
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG. 43

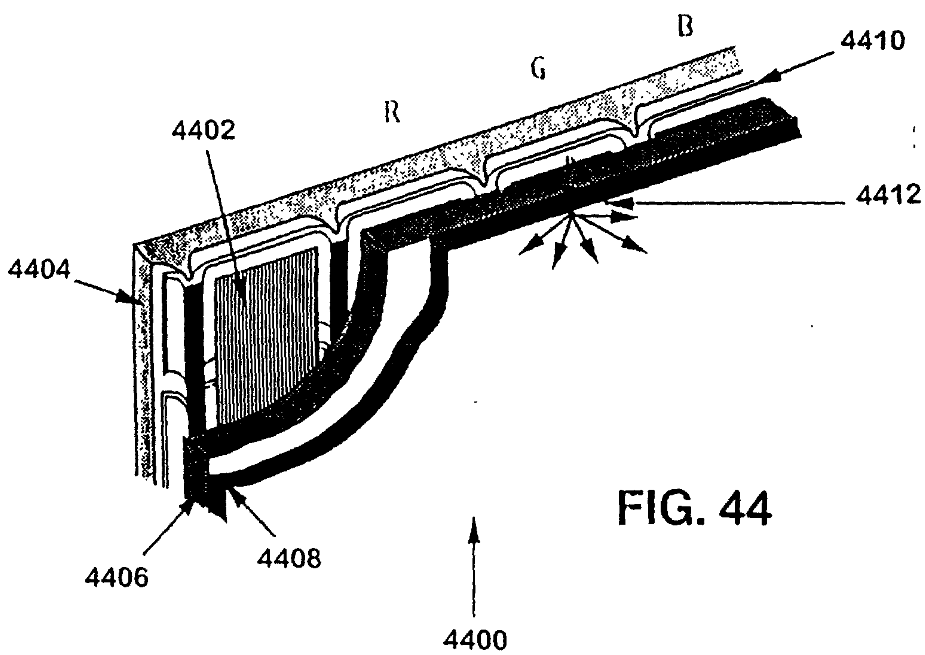
B
G
R
4410
4402
4412
4404
4406
4408
4400

FIG. 44

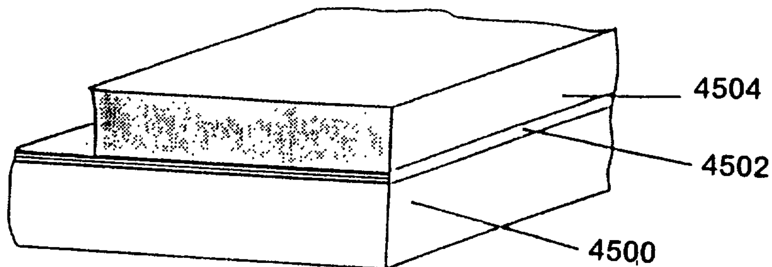

FIG. 45A

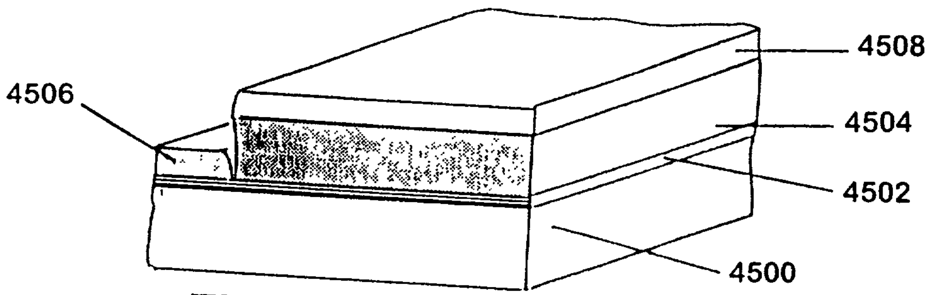

FIG. 45B

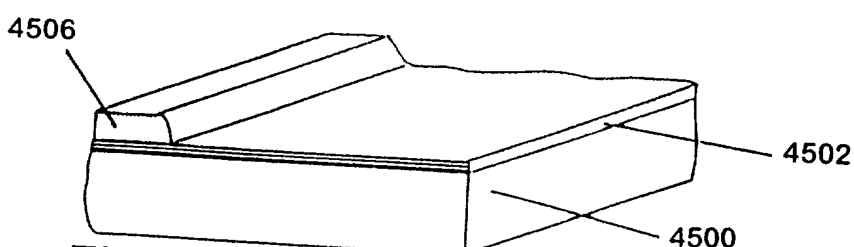

FIG. 45C

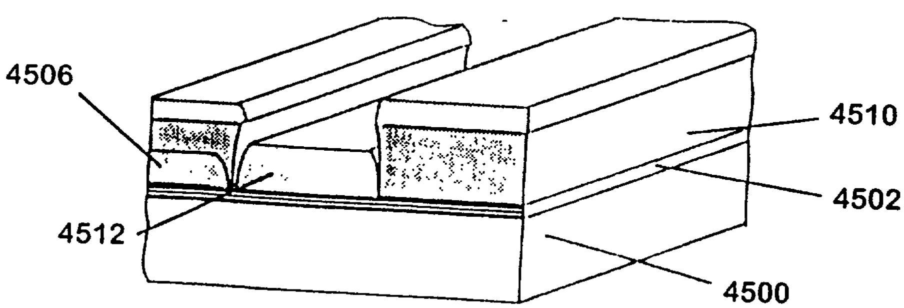

FIG. 45D

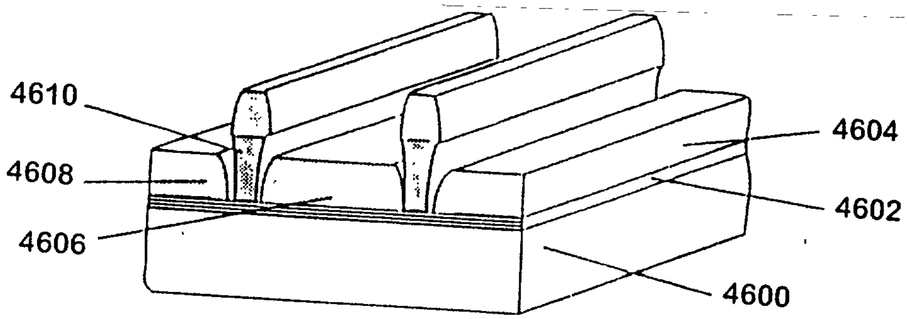

FIG. 46A

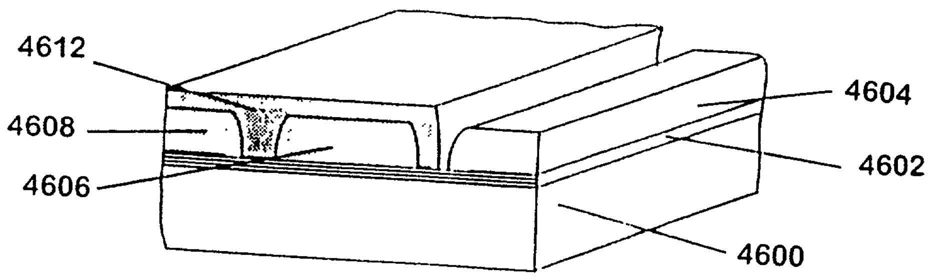

FIG. 46B

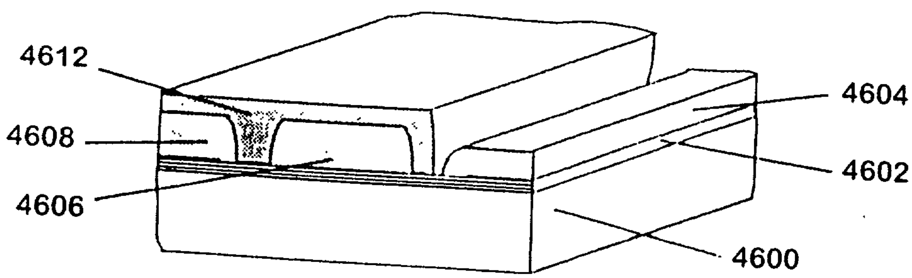

FIG. 46C

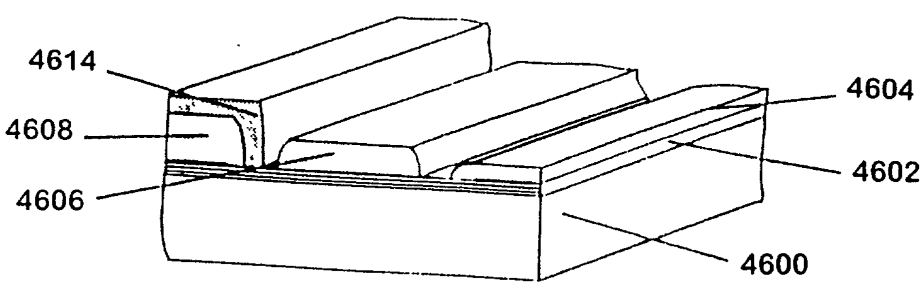

FIG. 46D

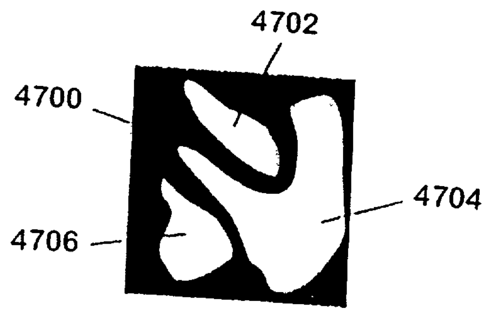

FIG. 47A

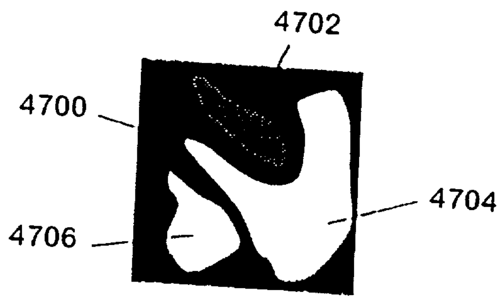

FIG. 47B

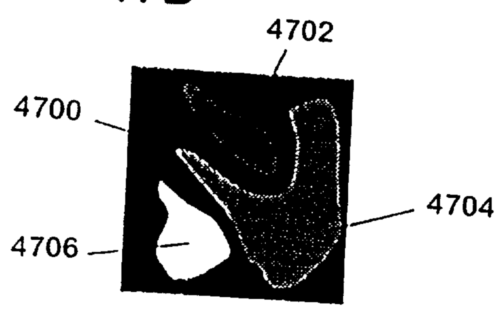

FIG. 47C

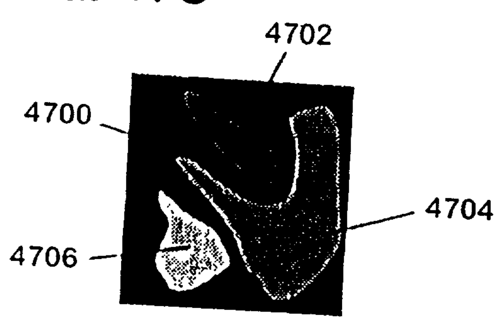

FIG. 47D